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- 1** [Modeling, Simulation and Performance Evaluation for a CIOQ Switch](#)  
 Antonio M. Alberti, Sebastiao B. de Aguiar Filho, Anilton S. Garcia  
 April 2006 **ANSS '06**: Proceedings of the 39th annual Symposium on Sit  
**Publisher:** IEEE Computer Society  
 Full text available: [Pdf](#) (534.38 KB) [Additional Information: full citation, ab:](#)  
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The intense growth experienced by the Internet on the past decade has switching architectures. Several architectures have been proposed and i (CIOQ) is one of such ...

- 2** [Queue: Volume 6 Issue 2](#)  
 March 2008 **Queue**  
**Publisher:** ACM  
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- 3** [A flexible simulation framework for graphics architectures](#)  
 J. W. Sheaffer, D. Luebke, K. Skadron  
 August 2004 **HWWS '04**: Proceedings of the ACM SIGGRAPH/EUROGRAPH  
**Publisher:** ACM  
 Full text available: [Pdf](#) (1.89 MB) [Additional Information: full citation, ab:](#)  
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In this paper we describe a multipurpose tool for analysis of the perform hardware and software. We are developing Qsilver, a highly configurabl uses the Chromium ...

- 4** [Transparent control independence \(TCI\)](#)  
 Ahmed S. Al-Zawawi, Vimal K. Reddy, Eric Rotenberg, Halitham H. Akkary  
 June 2007 **ISCA '07**: Proceedings of the 34th annual international symp  
**Publisher:** ACM  
 Full text available: [Pdf](#) (391.64 KB) [Additional Information: full citation, ab:](#)  
**Bibliometrics:** Downloads (6 Weeks): 34, Downloads (12 Months): 97, Downlo

Superscalar architectures have been proposed that exploit control indep branch mispredictions by preserving the work of future misprediction-in exploiting control ...

**Keywords:** branch prediction, checkpoints, control independence, select speculation

Also published in:

June 2007 **SIGARCH Computer Architecture News** Volume 35 Issue 2

5 [Communications of the ACM: Volume 52 Issue 11](#)

November 2009 Communications of the ACM



**Publisher:** ACM

Full text available: Digital Edition , Pdf (6.64 MB) Additional Information: [full citation](#)

**Bibliometrics:** Downloads (6 Weeks): 2744, Downloads (12 Months): 2744, Downloads (All Time): 2744

6 [Improving Gang Scheduling through job performance analysis and n](#)



Julita Corbalan, Xavier Martorell, Jesus Labarta

June 2001 **ICS '01: Proceedings of the 15th international conference on**

**Publisher:** ACM Request Permissions

Full text available: Pdf (150.61 KB)

Additional Information: [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 2, Downloads (12 Months): 29, Downloads (All Time): 29

The OpenMP programming model provides parallel applications a very ir malleability is the capacity of an application to dynamically adapt its pa it. We believe that ...

7 [Coyote: a system for constructing fine-grain configurable communica](#)



Nina T. Bhatti, Matti A. Hiltunen, Richard D. Schlichting, Wanda Chiu

November 1998 **Transactions on Computer Systems (TOCS)** , Volume 16 I

**Publisher:** ACM Request Permissions

Full text available: Pdf (290.21 KB)

Additional Information: [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 6, Downloads (12 Months): 46, Downloads (All Time): 46

Communication-oriented abstractions such as atomic multicast, group F mobile computing can simplify the development of complex applications describes Coyote, a system ...

**Keywords:** x-kernal, configurable sevicees, customization, event handle microprotocols, mobile computing, modularity, multicast, protocols, rer

8 [Dynamic warp formation: Efficient MIMD control flow on SIMD graph](#)



Wilson W. L. Fung, Ivan Sham, George Yuan, Tor M. Aamodt

June 2009 **Transactions on Architecture and Code Optimization (T**

**Publisher:** ACM Request Permissions

Full text available: Pdf (2.41 MB)

Additional Information: [full citation](#), [abstract](#)

**Bibliometrics:** Downloads (6 Weeks): 41, Downloads (12 Months): 238, Downloads (All Time): 238


Recent advances in graphics processing units (GPUs) have resulted in r programmable and widely available in today's desktop and notebook co instruction, multiple-data (SIMD) ...

**Keywords:** GPU, SIMD, control flow, fine-grained multithreading

## 9 Particle graphics on reconfigurable hardware

 John S. Beeckler, Warren J. Gross  
September 2008 **Transactions on Reconfigurable Technology and Systems**

**Publisher:** ACM  Request Permissions

Full text available:  Pdf (1.55 MB)


Additional Information: full citation, abstract

**Bibliometrics:** Downloads (6 Weeks): 10, Downloads (12 Months): 162, Downloads


Particle graphics simulations are well suited for modeling complex phenomena like smoke, and clouds. They are normally realized in software as part of an application with a high computational complexity ...

**Keywords:** FPGAs, Reconfigurable computing, particle systems, special effects

## 10 HNS: A streamlined hybrid network simulator

 Benjamin Melamed, Shuo Pan, Yoram Wardi  
July 2004 **Transactions on Modeling and Computer Simulation (TOMS)**

**Publisher:** ACM  Request Permissions

Full text available:  Pdf (865.29 KB)


Additional Information: full citation, abstract

**Bibliometrics:** Downloads (6 Weeks): 11, Downloads (12 Months): 51, Downloads


This article was motivated by the need to speed up complex network simulation in settings, where high bandwidth translates into exorbitant numbers of packets. Since the simulation ...

**Keywords:** Fluid-flow models, fluid TCP simulation, fluid-flow simulation models, streamlining

## 11 TinySec: a link layer security architecture for wireless sensor networks

 Chris Kariot, Naveen Sastry, David Wagner  
November 2004 **SenSys '04: Proceedings of the 2nd international conference on Sensor Networks**

**Publisher:** ACM  Request Permissions

Full text available:  Pdf (316.88 KB)

Additional Information: full citation, abstract


**Bibliometrics:** Downloads (6 Weeks): 68, Downloads (12 Months): 433, Downloads

We introduce TinySec, the first fully-implemented link layer security architecture. In this design, we leverage recent lessons learned from design vulnerabilities in protocols such as 802.11b ...

**Keywords:** link layer security, sensor network security

## 12 Pomegranate: a fully scalable graphics architecture

Matthew Eldridge, Homan Igehy, Pat Hanrahan  
July 2000 **SIGGRAPH '00: Proceedings of the 27th annual conference on Computer Graphics**

**Publisher:** ACM Press/Addison-Wesley Publishing Co.  Request Permissions

Full text available:

 Pdf (508.39 KB)

Additional Information: full citation, ab:

**Bibliometrics:** Downloads (6 Weeks): 7, Downloads (12 Months): 46, Downloa

Pomegranate is a parallel hardware architecture for polygon rendering t  
rate, pixel rate, texture memory and display bandwidth while maintainir  
of scalability ...

**Keywords:** graphics hardware, parallel computing

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